

Form PTO-1449		ATTORNEY DOCKET NO. RSW920030224US1	SERIAL NO. Not Assigned		
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Fricano et al.			
		FILING DATE January 15, 2004	GROUP ART UNIT Not Assigned		
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
SKS	AA 6,256,712 B1	Jul. 3, 2001	Challenger et al.	711/141	Aug. 1, 1997
S63	AB 4,021,784	May 3, 1977	Kimlinger	340/172.5	Mar. 12, 1976
S63	AC 4,827,401	May 2, 1989	Hrustich et al.	364/200	Oct. 24, 1984
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
S63	AD WO 99/40514	Aug. 12, 1999	WIPO	G06F 9/46	X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
S63	AE	Abali et al., "Clock Synchronization on a Multicomputer", Journal of Parallel and Distributed Computing, 40(1), 197, pages 1-31.			
S63	AF	Min et al., "Design and Analysis of a Scalable Cache Coherence Scheme Based on Clocks and Timestamps", IEEE Transactions on Parallel and Distributed Systems, Vol. 3, No. 1, Jan. 1992, pp. 25-44.			
S63	AG	IBM Research Disclosure, "Timestamp Method for Ensuring that Content Matches Notification in a Pull-Based Web Content Distribution System", April 2002, pp. 702-703.			
S63	AH	IBM Technical Disclosure Bulletin, "Dynamic Timer Algorithm for Caching Quota Information on OS/2 Distributed File System Client", Vol. 37, No. 04B, April 1994, pp. 467-469.			
S63	AI	IBM Technical Disclosure Bulletin, "Technique for Parallel Trace-Driven Multiprocessor Cache Simulation with Write Invalidate", Vol. 33, No. 3A, August 1990, pp. 174-178.			
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
DATE CONSIDERED 7/6/06		EXAMINER Suresh K. Suryawanshi			
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